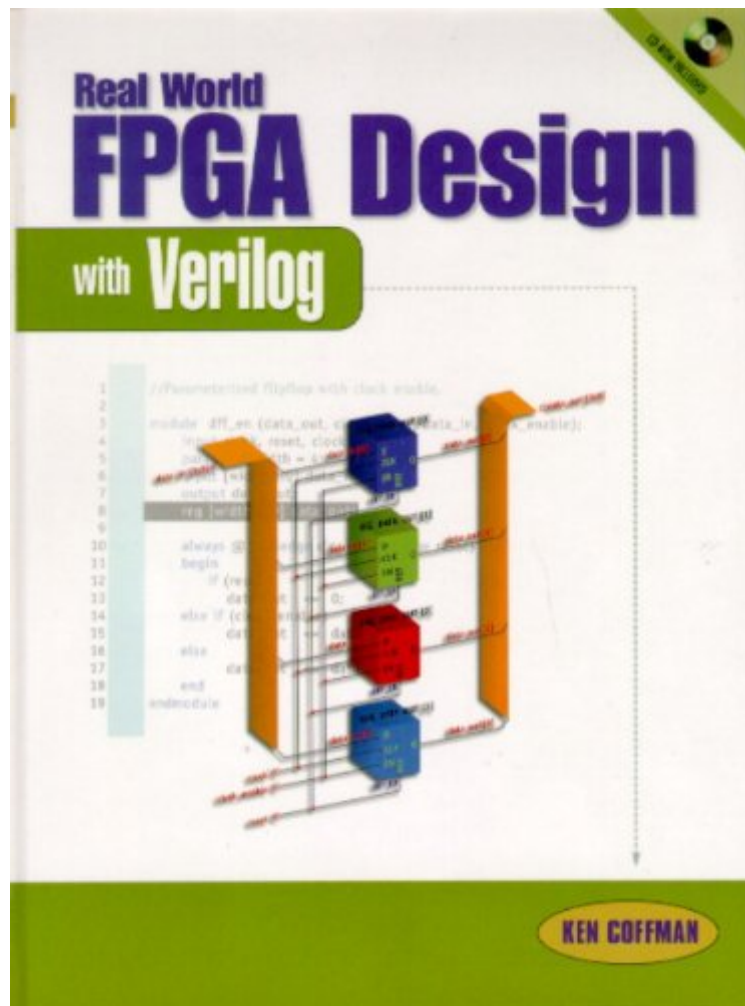


(Download ebook) Real World FPGA Design with Verilog (Prentice Hall Modern Semiconductor Design Series)

## Real World FPGA Design with Verilog (Prentice Hall Modern Semiconductor Design Series)

Von Ken Coffman

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**Von Ken Coffman : Real World FPGA Design with Verilog (Prentice Hall Modern Semiconductor Design Series)** before purchasing it in order to gage whether or not it would be worth my time, and all praised Real World FPGA Design with Verilog (Prentice Hall Modern Semiconductor Design Series):

KundenrezensionenHilfreichste Kundenrezensionen1 von 2 Kunden fanden die folgende Rezension hilfreich. It is a nice book for a FPGA design with VerilogVon Robert XueKen Coffman has 17 years experience with digital design. I have read through whole book in the passed few days. I have almost 5 years FPGA design experience with Nortel Network, Lucent Technologies. Through read this book, there are many interesting sections can enhance my design

knowledge. This book covered a lot of fields such as how does using the design tools, How does doing the statistic timing analysis, coding style etc. I think it is a nice book for all FPGA designers.

**Kurzbeschreibung**The practical guide for every circuit designer creating FPGA designs with Verilog! Walk through design step-by-step-from coding through silicon. Partitioning, synthesis, simulation, test benches, combinatorial and sequential designs, and more. Real World FPGA Design with Verilog guides you through every key challenge associated with designing FPGAs and ASICs using Verilog, one of the world's leading hardware design languages. You'll find irreverent, yet rigorous coverage of what it really takes to translate HDL code into hardware-and how to avoid the pitfalls that can occur along the way. Ken Coffman presents no-frills, real-world design techniques that can improve the stability and reliability of virtually any design. Start by walking a typical Verilog design all the way through to silicon; then, review basic Verilog syntax, design; simulation and testing, advanced simulation, and more. Coverage includes: Essential digital design strategies: recognizing the underlying analog building blocks used to create digital primitives; implementing logic with LUTs; clocking strategies, logic minimization, and more Key engineering tradeoffs, including operating speed vs. latency Combinatorial and sequential designs Verilog test fixtures: compiler directives and automated testing A detailed comparison of alternative architectures and software-including a never-before-published FPGA technology selection checklist Real World FPGA Design with Verilog introduces libraries and reusable modules, points out opportunities to reuse your own code, and helps you decide when to purchase existing IP designs instead of building from scratch. Essential rules for designing with ASIC conversion in mind are presented. If you're involved with digital hardware design with Verilog, Ken Coffman is a welcome voice of experience-showing you the shortcuts, helping you over the rough spots, and helping you achieve competence faster than you ever expected!

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