Reconfigurable Computing: From FPGAs to Hardware/Software Codesign

Von Springer
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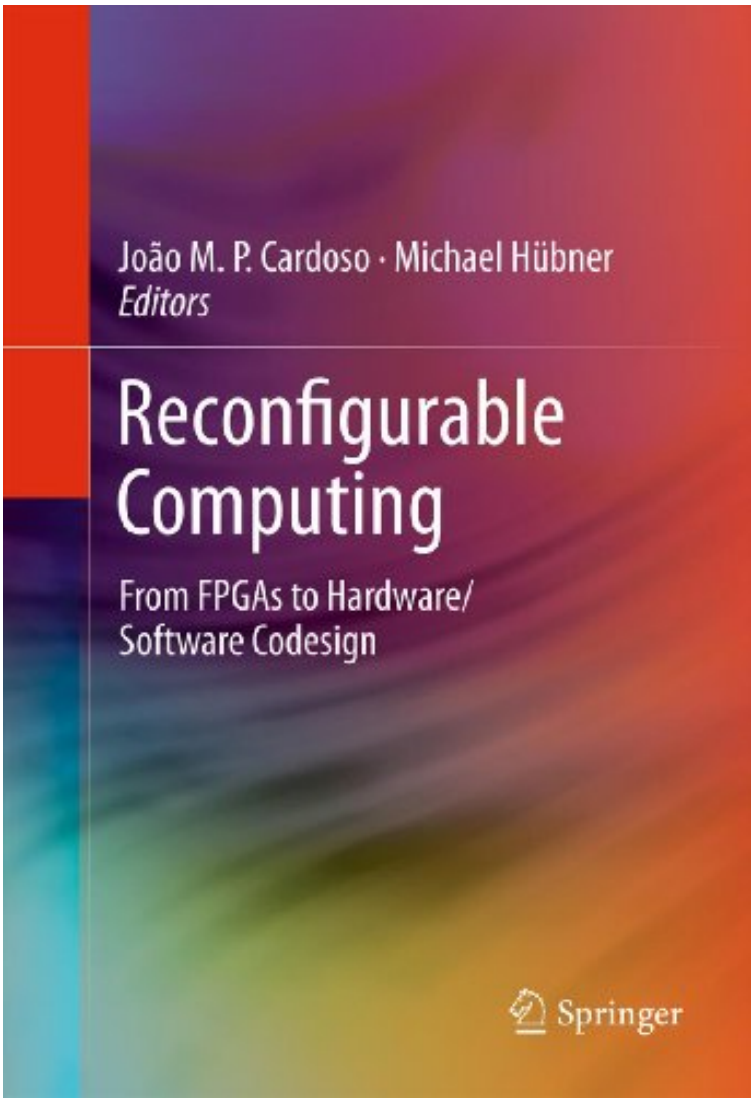

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Von Springer : Reconfigurable Computing: From FPGAs to Hardware/Software Codesign before purchasing it in order to gage whether or not it would be worth my time, and all praised Reconfigurable Computing: From FPGAs to Hardware/Software Codesign:

KundenrezensionenHilfreichste Kundenrezensionen0 von 0 Kunden fanden die folgende Rezension hilfreich. Licht und SchattenVon Olivier HessErstens Mal ist das Buch sndhaft teuer, aber machte mich neugierig. Es werden 12 Projekte vorgestellt, die von der EU mit erhablichen Mitteln finanziert wurden. Die Universitten in den EU Lndern

sind sehr unterschiedlich vertreten, die Niederlande z.B. extrem, andere Lnder wie Belgien gar nicht. Man hat den Eindruck, dass das Thema polarisiert. Die Ergebnisse sind zum Teil fragwrdig, wenn z.B. mit Millionenmitteln ein MPEG2 Dekoder entworfen wird, und das auch noch nur fr den Ton. Andererseits gibt es fr Satellitennavigation einen "Hexium" Prozessor (sechs Kerne), was sich schon ganz anders anhrt.

KurzbeschreibungAs the complexity of modern embedded systems increases, it becomes less practical to design monolithic processing platforms. As a result, reconfigurable computing is being adopted widely for more flexible design. Reconfigurable Computers offer the spatial parallelism and fine-grained customizability of application-specific circuits with the postfabrication programmability of software. To make the most of this unique combination of performance and flexibility, designers need to be aware of both hardware and software issues. FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process. The goal of this book is to help designers become comfortable with these issues, and thus be able to exploit the vast opportunities possible with reconfigurable logic.KurzbeschreibungAs the complexity of modern embedded systems increases, it becomes less practical to design monolithic processing platforms. As a result, reconfigurable computing is being adopted widely for more flexible design. Reconfigurable Computers offer the spatial parallelism and fine-grained customizability of application-specific circuits with the postfabrication programmability of software. To make the most of this unique combination of performance and flexibility, designers need to be aware of both hardware and software issues. FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process. The goal of this book is to help designers become comfortable with these issues, and thus be able to exploit the vast opportunities possible with reconfigurable logic.BuchrckseiteAs the complexity of modern embedded systems increases, it becomes less practical to design monolithic processing platforms. As a result, reconfigurable computing is being adopted widely for more flexible design. Reconfigurable Computers offer the spatial parallelism and fine-grained customizability of application-specific circuits with the postfabrication programmability of software. To make the most of this unique combination of performance and flexibility, designers need to be aware of both hardware and software issues. FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process. The goal of this book is to help designers become comfortable with these issues, and thus be able to exploit the vast opportunities possible with reconfigurable logic.Focuses on both hardware and software systemsTreats FPGAs as computing vehicles rather than glue-logic or ASIC substitutesAssembles broad set of models for exploiting FPGA parallelismDemonstrates how to use and manage reconfigurationProvides broad set of case studies demonstrating how to use FPGAs in novel and efficient ways